

A mass storage data protection system for use with a mass storage device in a gaming machine to protect the mass storage device from unauthorized commands. The mass storage data protection system includes a mass storage device command latch, a timing circuit for timing signals between the mass storage device command latch and the mass storage device, a comparator and a comparator command register in communication with the comparator. The comparator command register includes commands that generate a fault within the mass storage data protection system such that when the comparator receives a command from the mass storage device command latch corresponding to the command within the comparator command register, a fault is generated within the mass storage command latch.